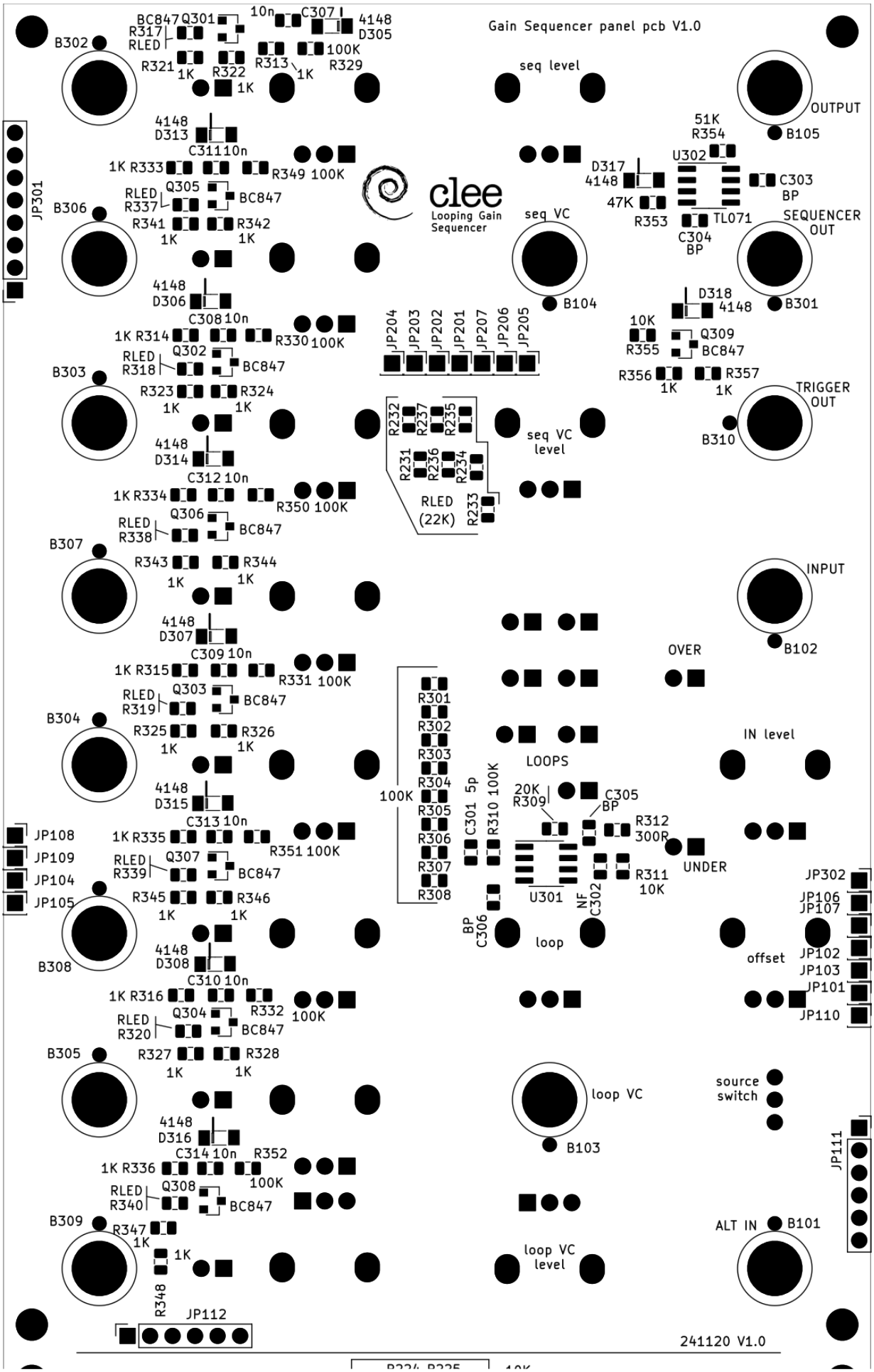
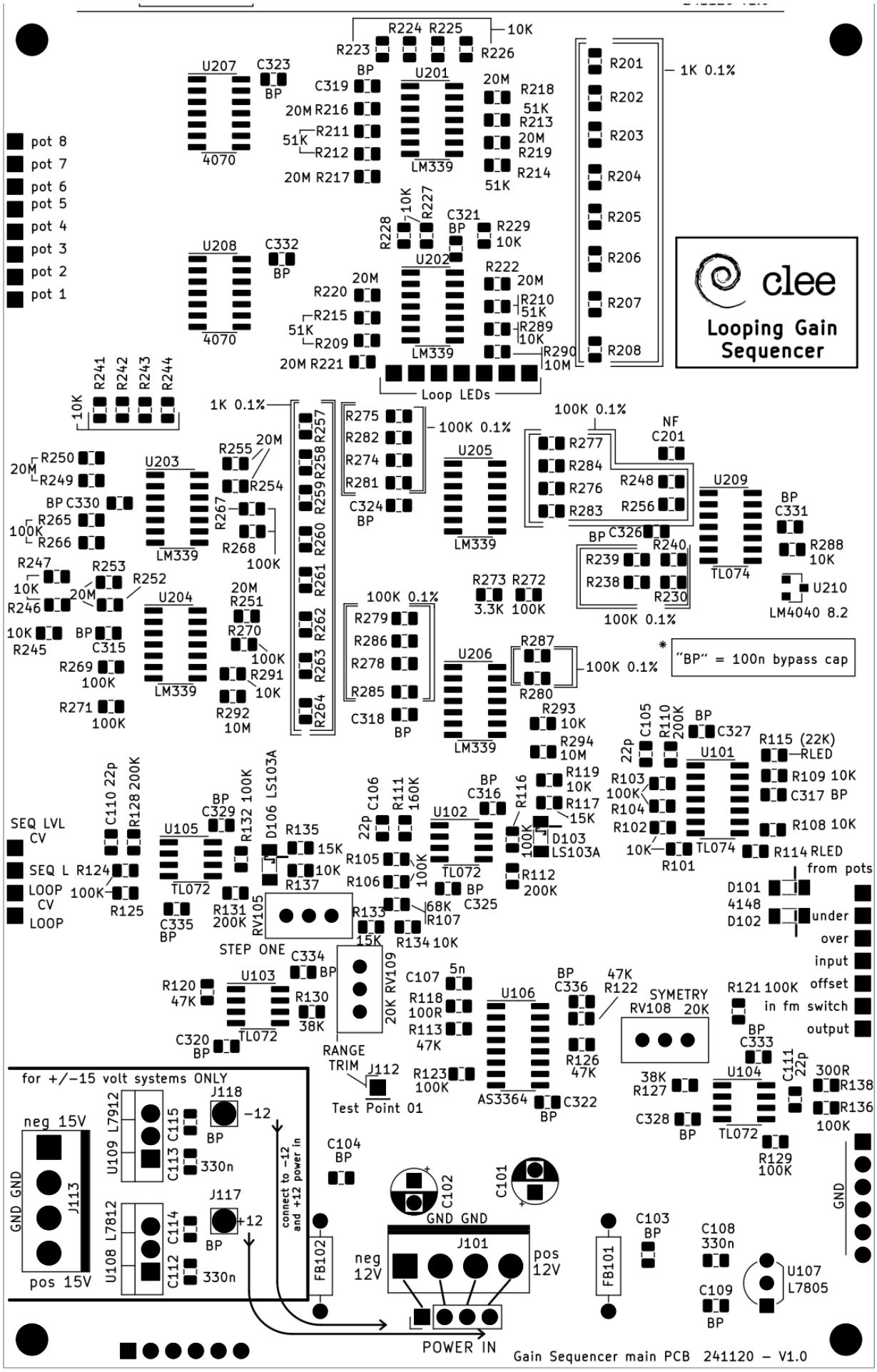

Gain Sequencer panel pcb V1.0
clee
Looping Gain Sequencer
seq level
OUTPUT
B105
51K R354
U302
D317 4148
C303 BP
47K R353
TL071
C304 BP
SEQUENCER OUT
B301
D318 4148
10K R355
Q309 BC847
R356 1K
R357 1K
TRIGGER OUT
B310
seq VC
B104
seq VC level
RLED (22K)
R232 R237 R235 R231 R236 R234 R233
JP204 JP203 JP202 JP201 JP207 JP206 JP205
JP301
B302 B306 B303 B307 B304 B308 B305 B309
BC847 Q301 R317 RLED
10n C307 4148 D305
R321 1K R322 1K R313 1K R329 100K
4148 D313 C311 10n
1K R333 R349 100K
RLED R337 Q305 BC847
R341 1K R342 1K
4148 D306 C308 10n
1K R314 R330 100K
RLED R318 Q302 BC847
R323 1K R324 1K
4148 D314 C312 10n
1K R334 R350 100K
RLED R338 Q306 BC847
R343 1K R344 1K
4148 D307 C309 10n
1K R315 R331 100K
RLED R319 Q303 BC847
R325 1K R326 1K
4148 D315 C313 10n
1K R335 R351 100K
RLED R339 Q307 BC847
R345 1K R346 1K
4148 D308 C310 10n
1K R316 R332 100K
RLED R320 Q304 BC847
R327 1K R328 1K
4148 D316 C314 10n
1K R336 R352 100K
RLED R340 Q308 BC847
R347 1K
1K
R348
JP112
INPUT
B102
OVER
IN level
LOOPS
R301 R302 R303 R304 R305 R306 R307 R308
100K
C301 5p
R310 100K
20K R309
C305 BP
R312 300R
UNDER
R311 10K
U301
NF C302
BP C306
loop
offset
JP108 JP109 JP104 JP105
JP302 JP106 JP107 JP102 JP103 JP101 JP110
loop VC
B103
source switch
JP111
ALT IN
B101
loop VC level
241120 V1.0


clee
Looping Gain Sequencer
pot 8
pot 7
pot 6
pot 5
pot 4
pot 3
pot 2
pot 1
Loop LEDs
1K 0.1%
100K 0.1%
"BP" = 100n bypass cap
SEQ LVL
CV
SEQ L
LOOP CV
LOOP
STEP ONE
RANGE TRIM
Test Point 01
for +/−15 volt systems ONLY
neg 15V
pos 15V
GND GND
connect to −12 and +12 power in
neg 12V
pos 12V
POWER IN
from pots
under
over
input
offset
in fm
switch
output
SYMETRY
Gain Sequencer main PCB 241120 – V1.0